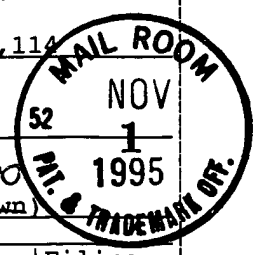


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FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

Attorney Docket 6605-9	Serial Number 08/478,114
Applicant Paul Poenisch et al.	
Filing Date June 7, 1995	Group 1100 (Unknown)



U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
<i>P</i>	AA	4,875,617	10/24/89	Citowsky	228	123	
<i>P</i>	AB	5,075,965	12/31/91	Carey et al.	29	840	
<i>P</i>	AC	5,108,027	4/28/92	Warner et al.	228	254	
<i>P</i>	AD	5,147,822	9/15/92	Yamazaki et al.	437	215	
<i>P</i>	AE	5,149,671	9/22/92	Koh et al.	437	183	
<i>P</i>	AF	5,168,346	12/1/92	Pasch et al.	257	738	
<i>P</i>	AG	5,183,972	2/2/93	Duane et al.	174	251	
<i>P</i>	AH	5,237,130	8/17/93	Kulesza et al.	174	260	
<i>P</i>	AI	5,249,733	10/5/93	Brady et al.	228	180.22	
<i>P</i>	AJ	5,299,730	4/5/94	Pasch et al.	228	180.22	
<i>P</i>	AK	5,344,607	9/6/94	Gonya et al.	420	562	

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Translation
	AL		/ /				Yes
	AM		/ /				No
	AN		/ /				Yes
	AO		/ /				No
	AP		/ /				Yes

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>P</i>	AR		Caillat et al., "Fluxless Flip-Chip Technology", 1994 ITAR & Flip Chip Proceedings, pp. 32-36.
<i>P</i>	AS		Clementi et al., "Flip-Chip Assembly on CQFP Using No-Clean Processing", '95 Flip Chip, BGA, TAB & AP Symposium, pp. 1-6.
<i>P</i>	AT		Kallmayer et al., "Experimental Results on the Self-Alignment Process Using Au/Sn Metallurgy and on the Growth of the ζ -Phase During the Reflow", '95 Flip Chip, BGA, TAB & AP Symposium, pp. 225-236.

Examiner
GRAYBILL

Date Considered
3-17-97

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6605-9

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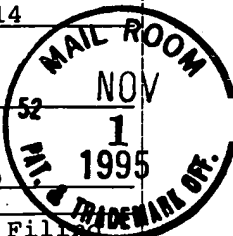
Applicant

Paul Poenisch et al.

Filing Date

June 7, 1995

Group

1100
(Unknown)U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
<i>Q</i>	AA	5,346,857	9/13/94	Scharr et al.	437	183	
<i>Q</i>	AB	5,359,768	11/1/94	Haley	29	840	
<i>Q</i>	AC	5,366,692	11/22/94	Ogashiwa	420	565	
<i>Q</i>	AD	5,368,217	11/29/94	Simmons et al.	228	6.2	
<i>Q</i>	AE	5,368,814	11/29/94	Gonya et al.	420	587	
<i>Q</i>	AF	5,377,902	1/3/95	Hayes	228	254	
<i>Q</i>	AG	5,381,039	1/10/95	Morrison	257	701	
<i>Q</i>	AH	5,385,869	1/31/95	Liu et al.	437	209	
<i>Q</i>	AI	5,391,514	2/21/95	Gall et al.	437	183	
<i>Q</i>	AJ	5,393,489	2/28/95	Gonya et al.	420	561	
<i>Q</i>	AK	5,393,696	2/28/95	Koh et al.	437	183	

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Translation
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	AM	/ / /	/ / /				No
	AN	/ / /	/ / /				Yes
	AO	/ / /	/ / /				No
	AP	/ / /	/ / /				Yes
							No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>Q</i>	AR		Gupta, "Application of Thermal and Kinetic Modeling to the Reflow and Flip Chip Bonding of Bumps using the Au - Sn - Au, '95 Flip Chip, BGA, TAB & AP Symposium, pp. 164-172.
	AS		
	AT		

Examiner

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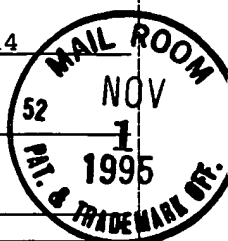
Serial Number

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(Unknown)

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
<i>A</i>	AA	5,404,045	4/4/95	Mizushima	257	698	
<i>B</i>	AB	5,404,265	4/4/95	Moresco et al.	361	306.1	
<i>C</i>	AC	5,406,701	4/18/95	Pepe et al.	29	840	
<i>D</i>	AD	5,407,121	4/18/95	Koopman et al.	228	206	
<i>E</i>	AE	5,410,805	5/2/95	Pasch et al.	29	830	
<i>F</i>	AF	5,411,703	5/2/95	Gonya et al.	420	561	
	AG	, ,	/ /				
	AH	, ,	/ /				
	AI	, ,	/ /				
	AJ	, ,	/ /				
	AK	, ,	/ /				

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Trans-lation
	AL	, ,	/ /				Yes
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	AM	, ,	/ /				Yes
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	AN	, ,	/ /				Yes
							No
	AO	, ,	/ /				Yes
							No
	AP	, ,	/ /				Yes
							No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR		
	AS		
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Examiner

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3-17-97

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